Serial Number: 10/716,320 Filing Date: November 17, 2003

Title: A MEHTOD AND SYSTEM FOR AUTOMATICALLY CALIBRATING INTRA-CYCLE TIMING RELATIONSHIPS FOR SAMPLING SIGNALS FOR AN INTEGRATED CIRCUIT DEVICE

S/N 10/716,320 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

 Applicant:
 Rozas G.
 Examiner: Odom, C.

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Title: A METHOD AND SYSTEM FOR AUTOMATICALLY CALIBRATING INTRA-CYCLE TIMING RELATIONSHIPS FOR SAMPLING SIGNALS FOR AN INTEGRATED

CIRCUIT DEVICE

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicant has reviewed the Office Action mailed on December 12th, 2008.

Please consider the following amendments and remarks.